

FIG. 1

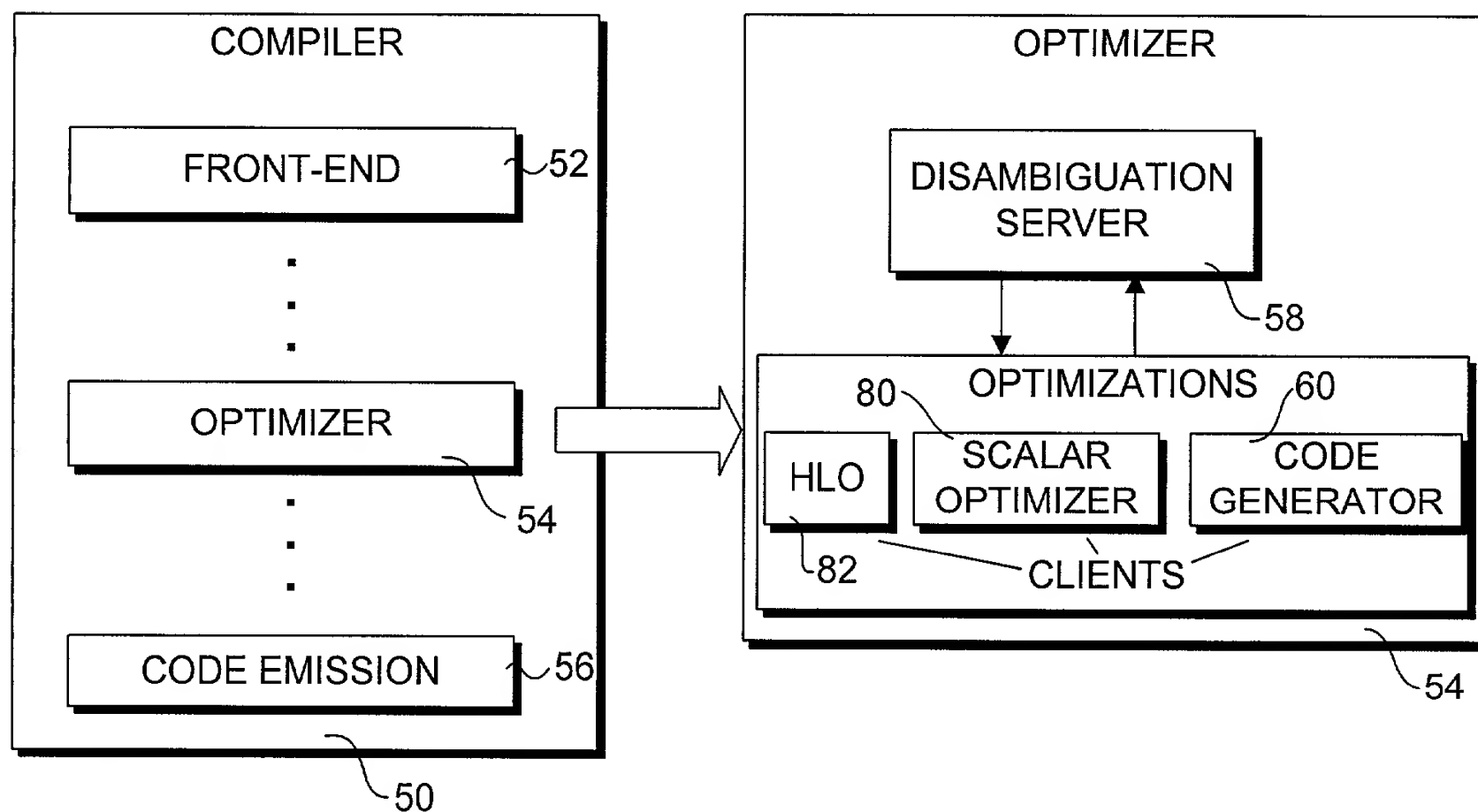


FIG. 5

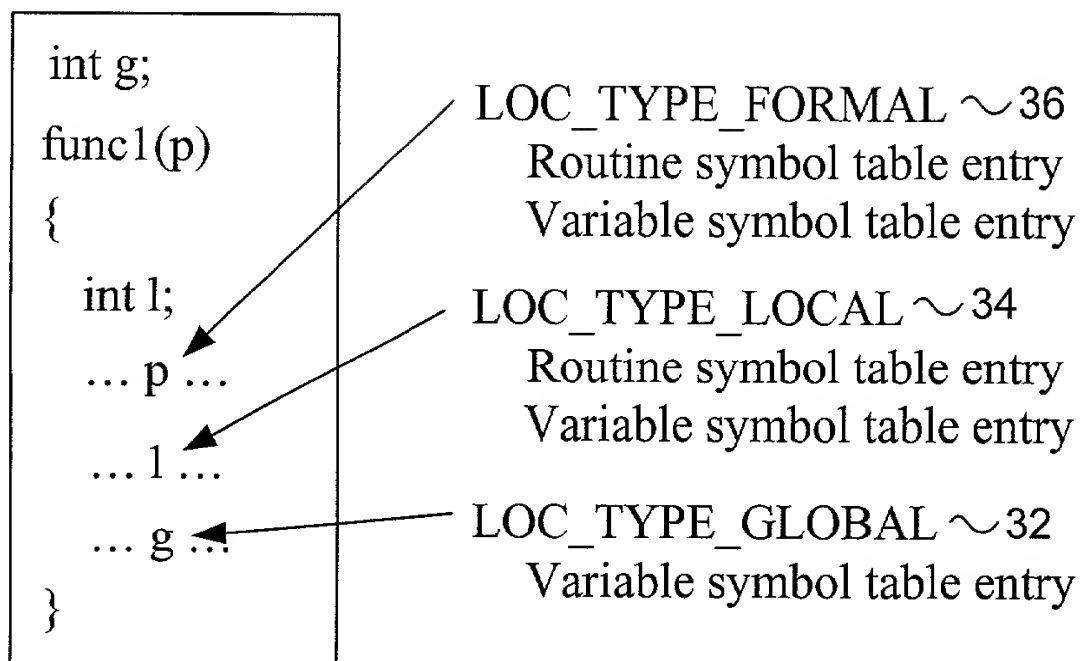


FIG. 2A

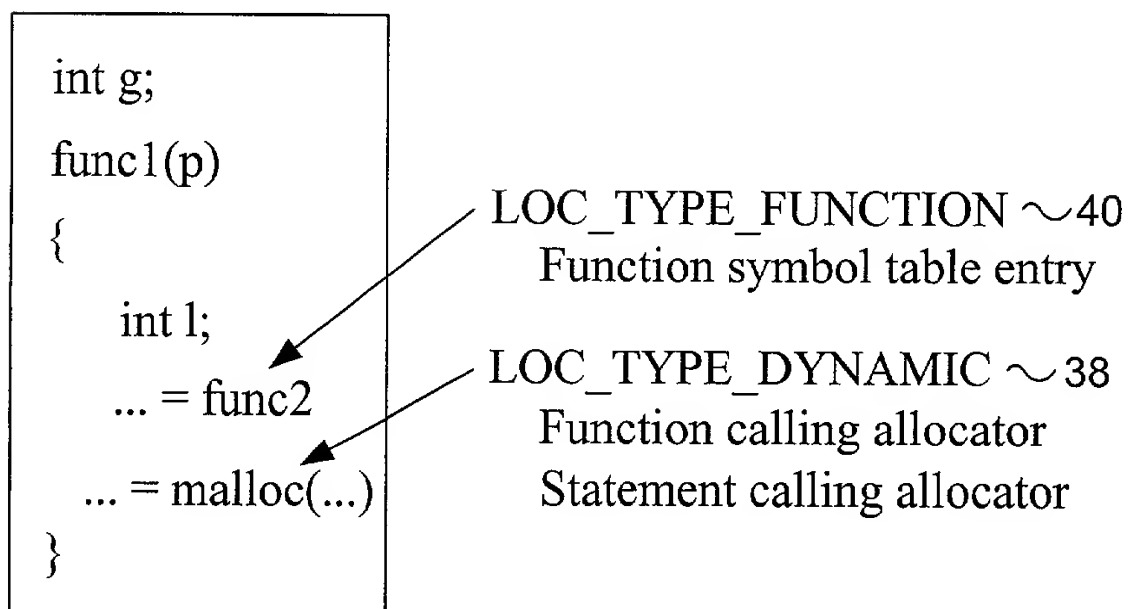


FIG. 2B

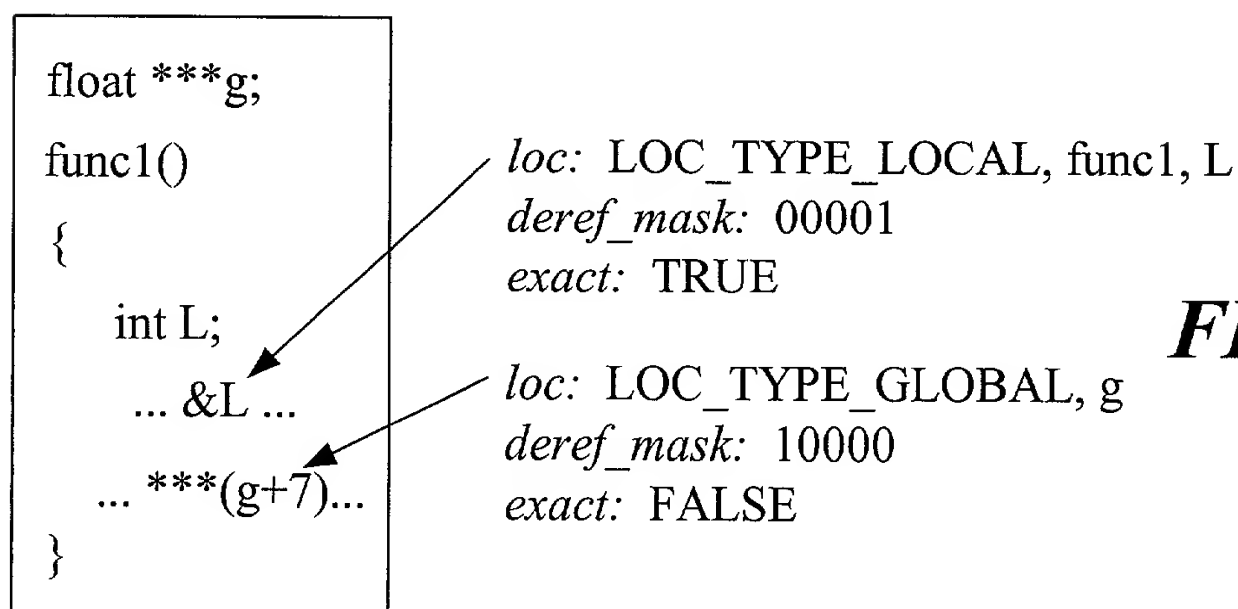


FIG. 2C

struct {int field1, int field2} node;

Intermediate Language
Memory Reference
load or store
node.field1

Direct Reference

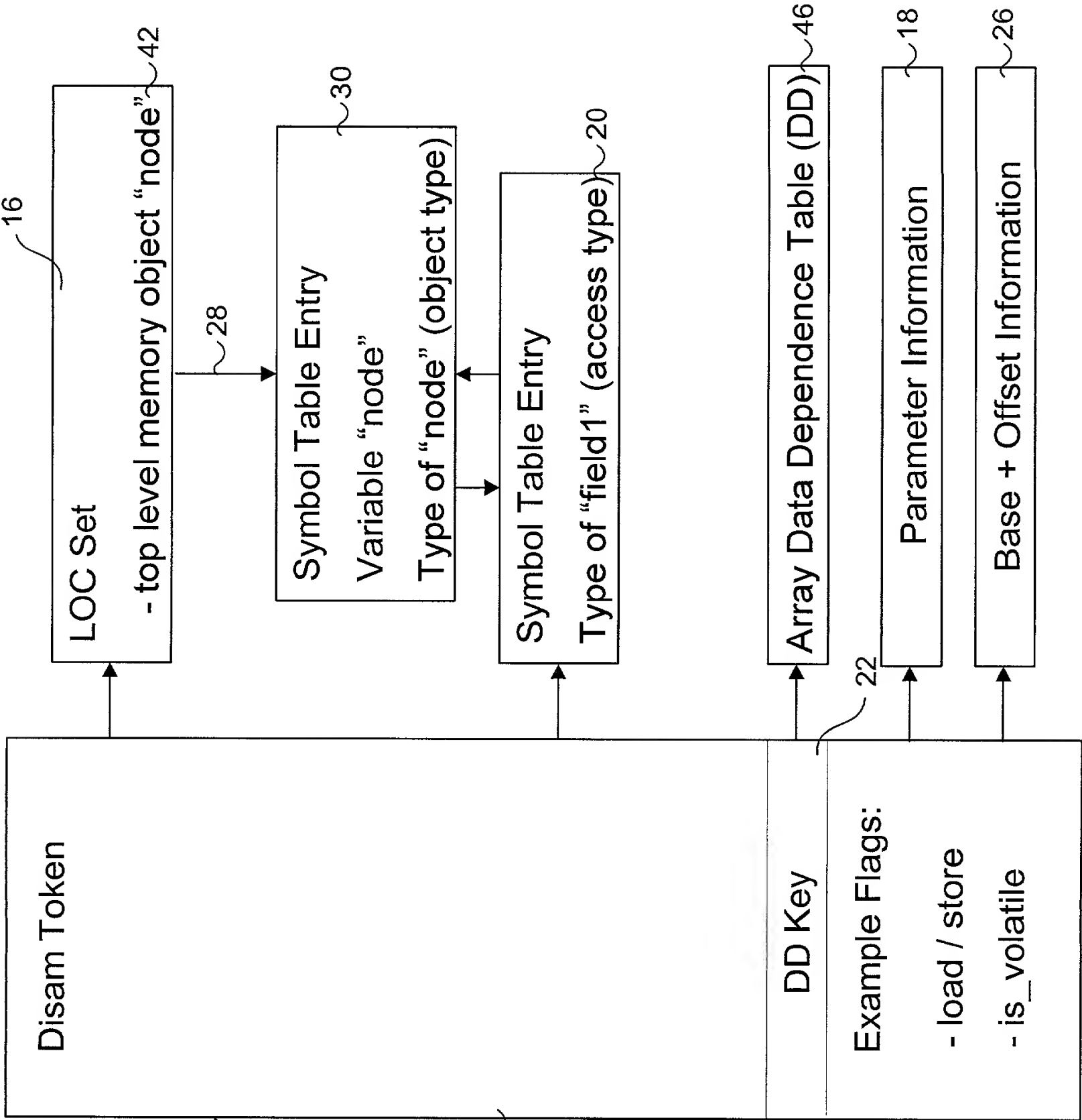


FIG. 3

struct {int field1, int field2} node;

Intermediate Language
Memory Reference
load or store
ptr->field1 or (*ptr).field1

Indirect Reference

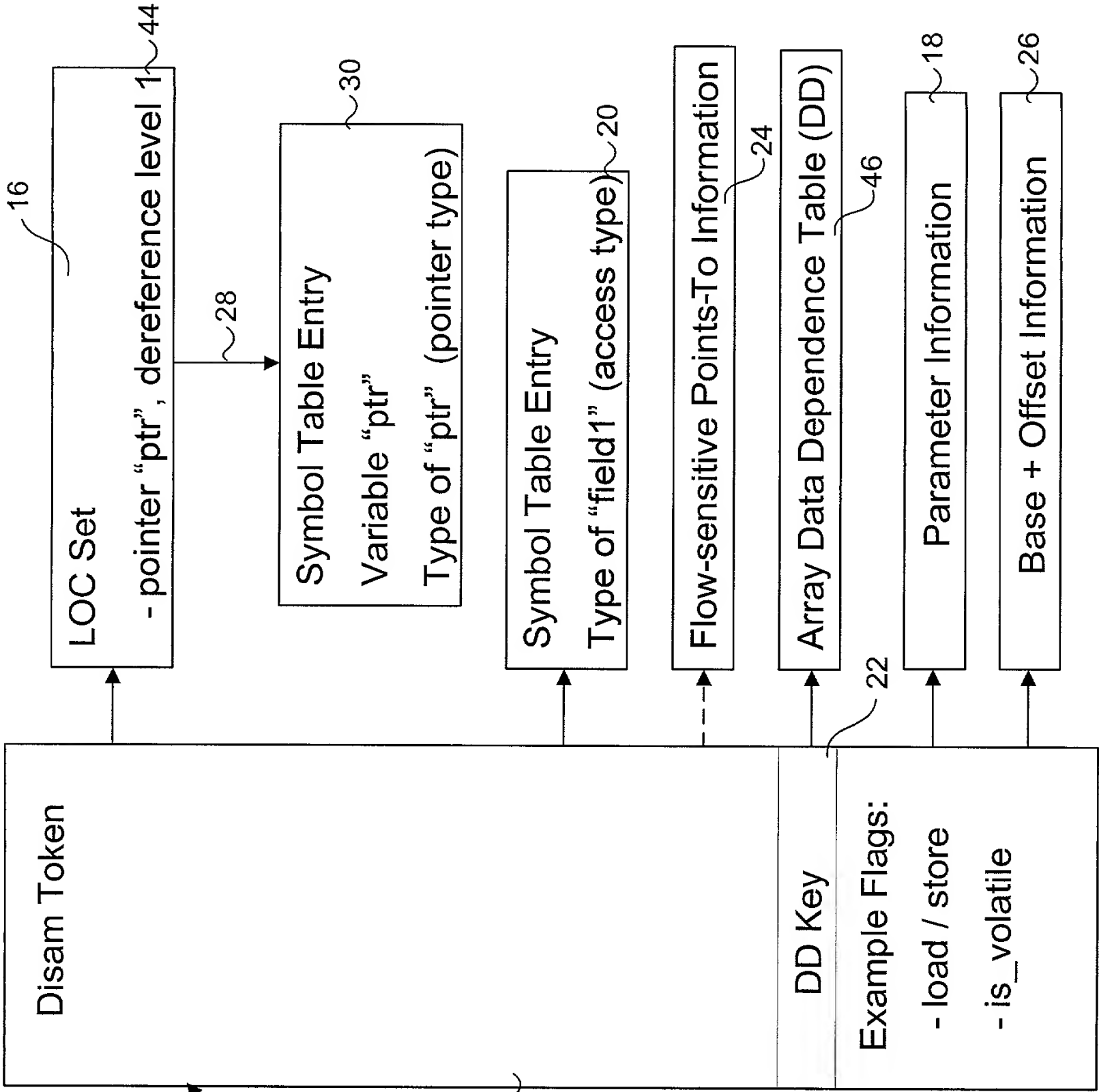


FIG. 4

FIG. 6 is a block diagram of a disambiguation server 68. The disambiguation server 68 includes a base + offset analysis block 66, a flow-insensitive points-to block 66, an array data dependence table 64, a disambiguator block 62, a flow-sensitive points-to block 70, a function call modification block 74, a parameter copy and modification analysis block 72, a symbol table 76, and a scalar optimizer clients block 80. The disambiguation server 68 is connected to HLO clients 82 and schedulers local, global, and pipeliner 78. The disambiguation server 68 is also connected to a legend 58.

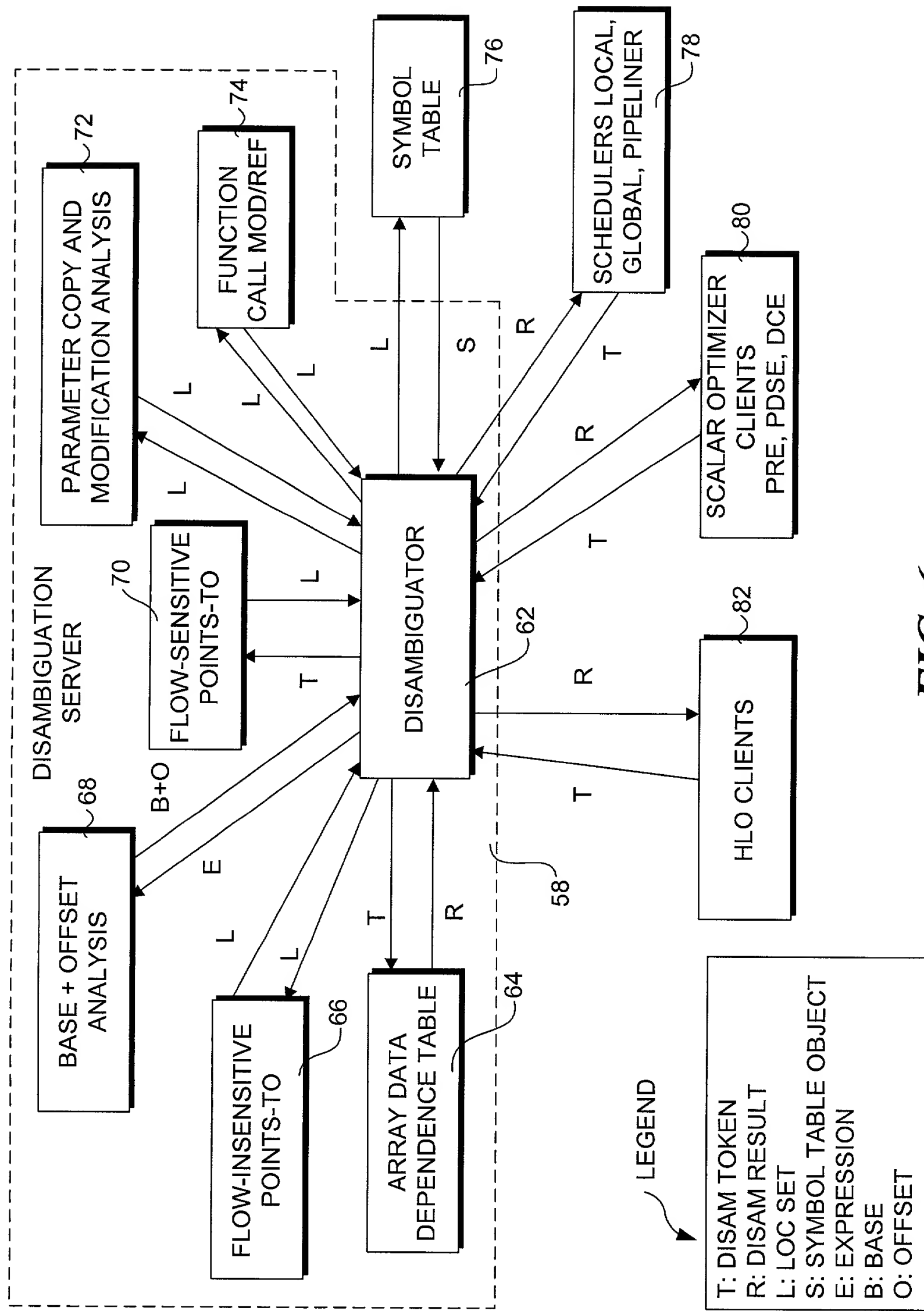
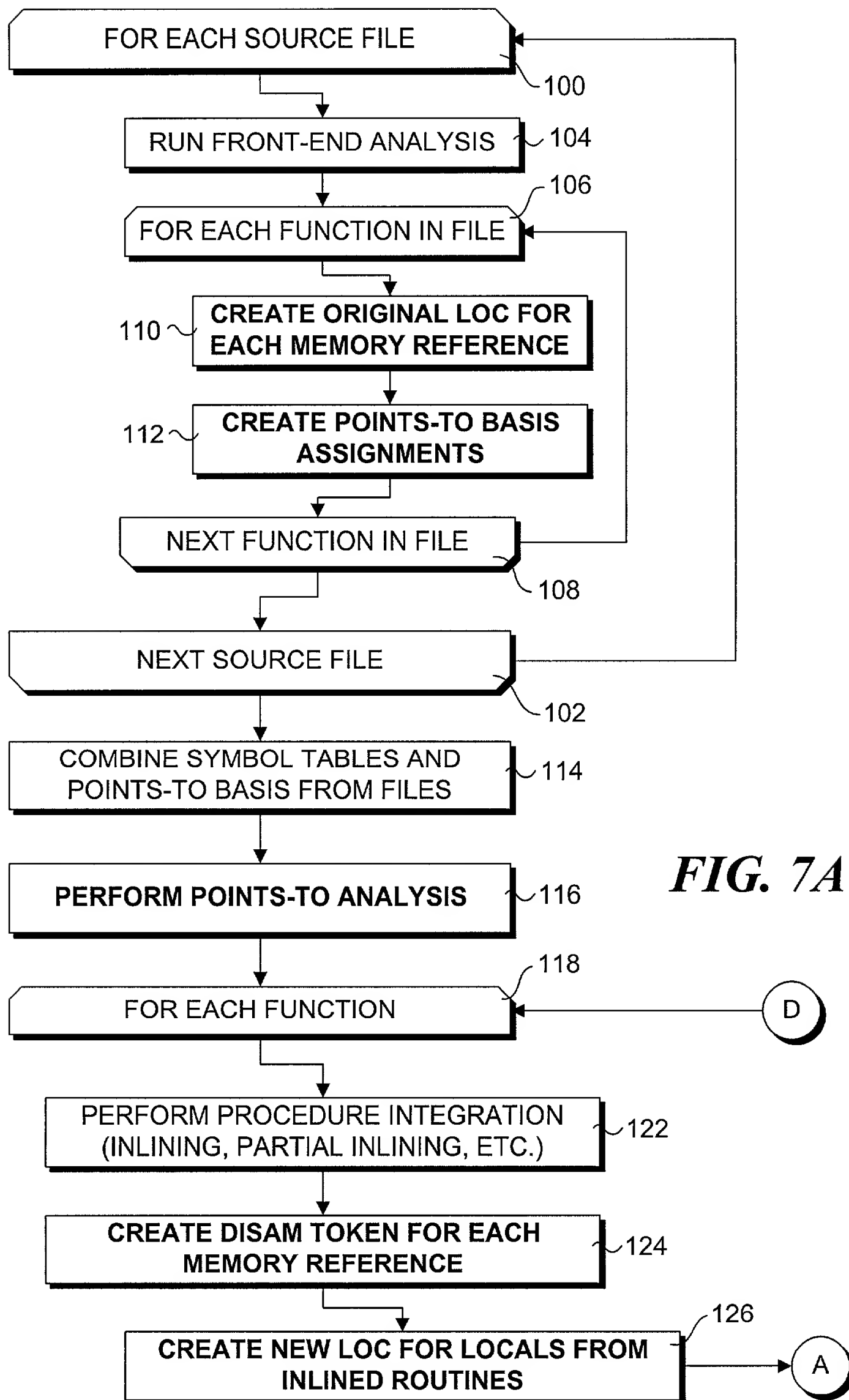


FIG. 6



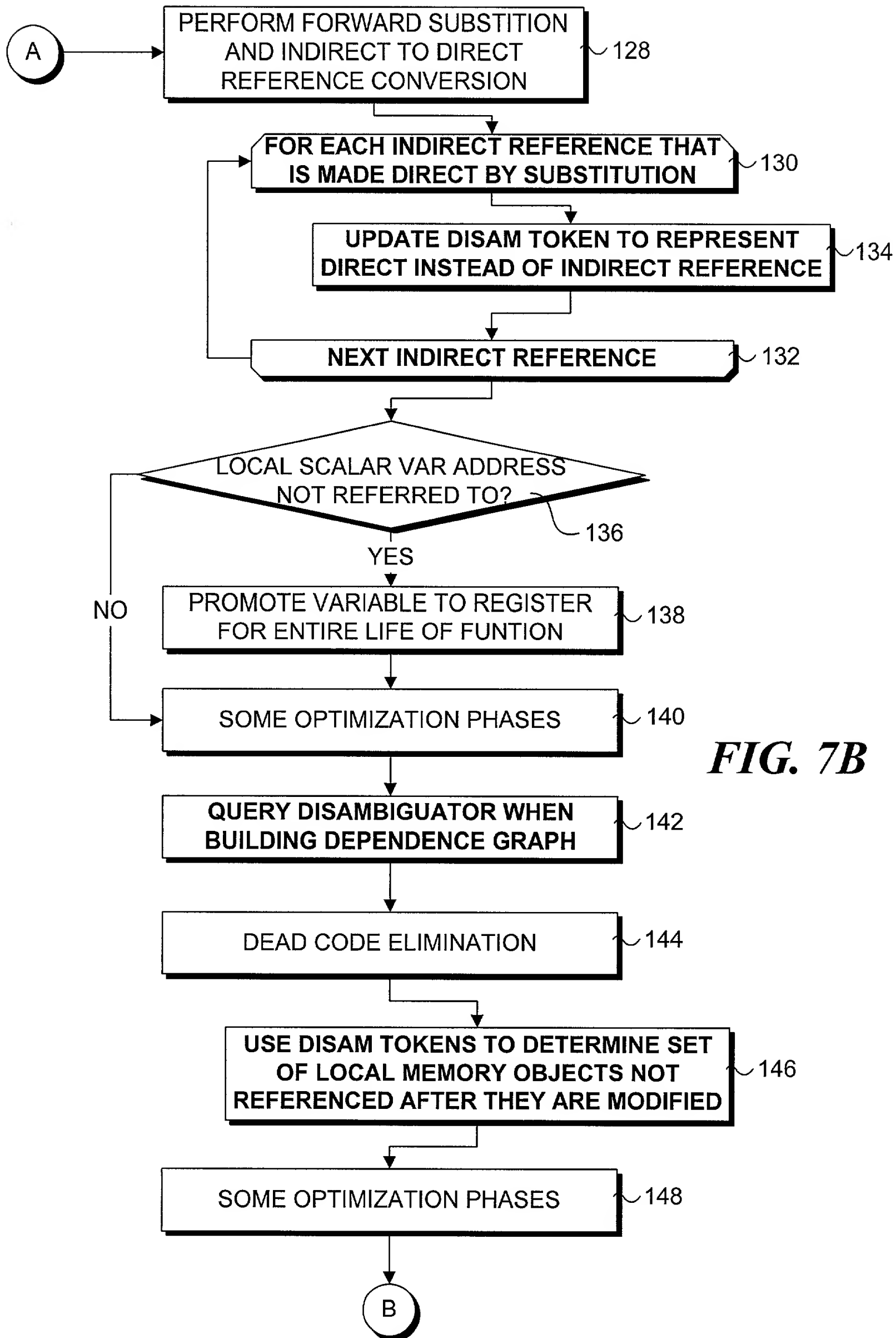


FIG. 7B

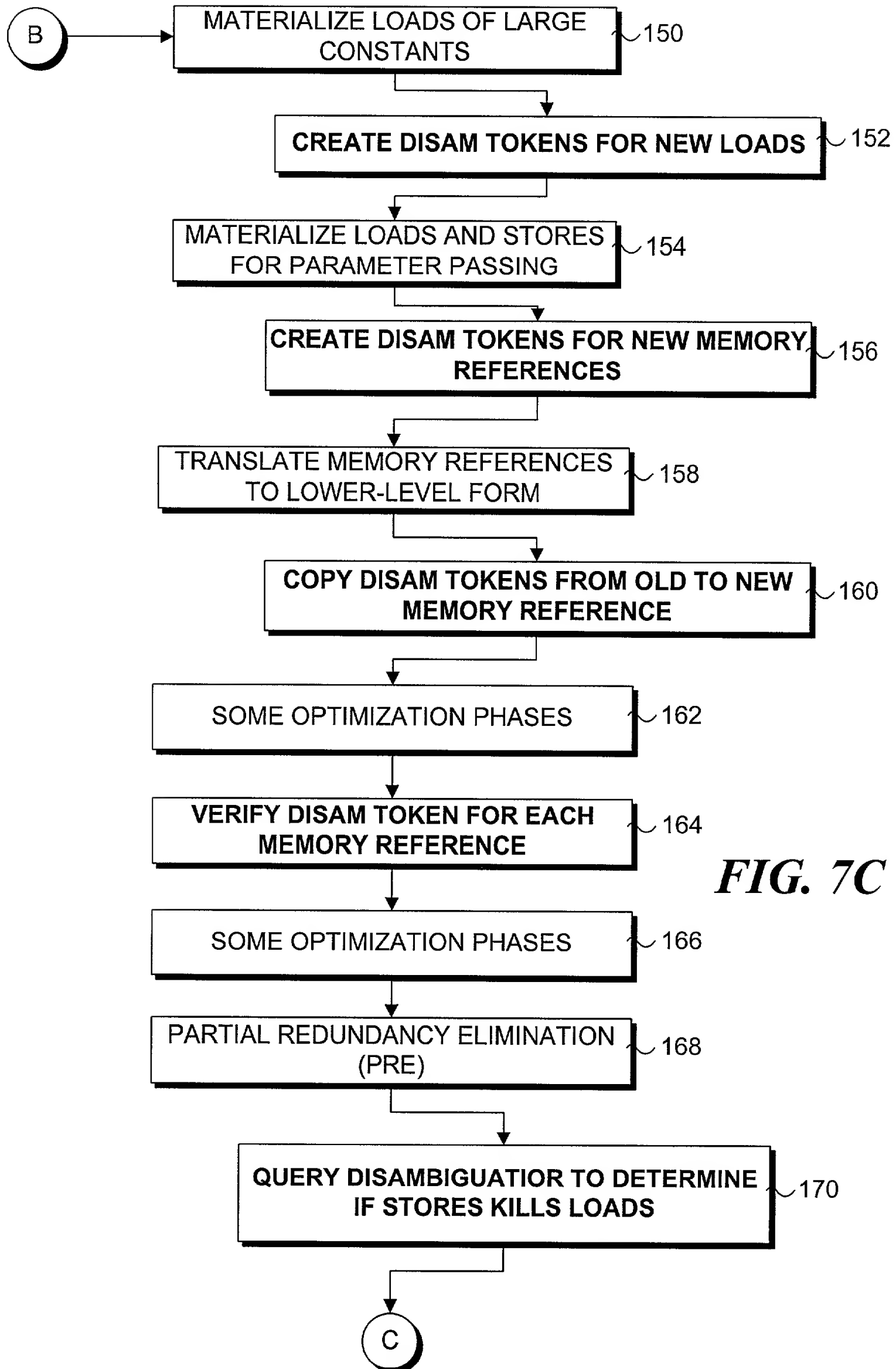
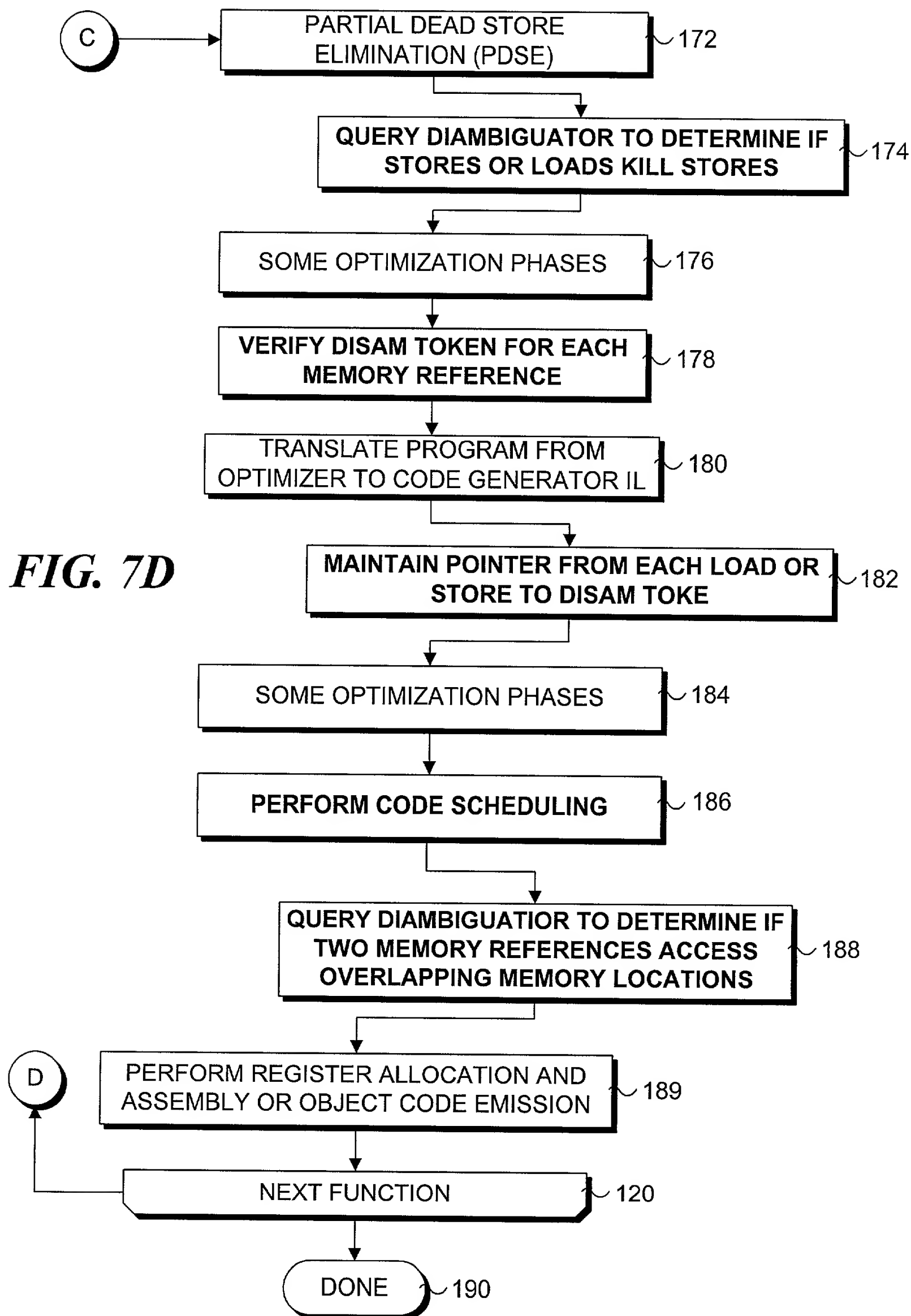


FIG. 7C



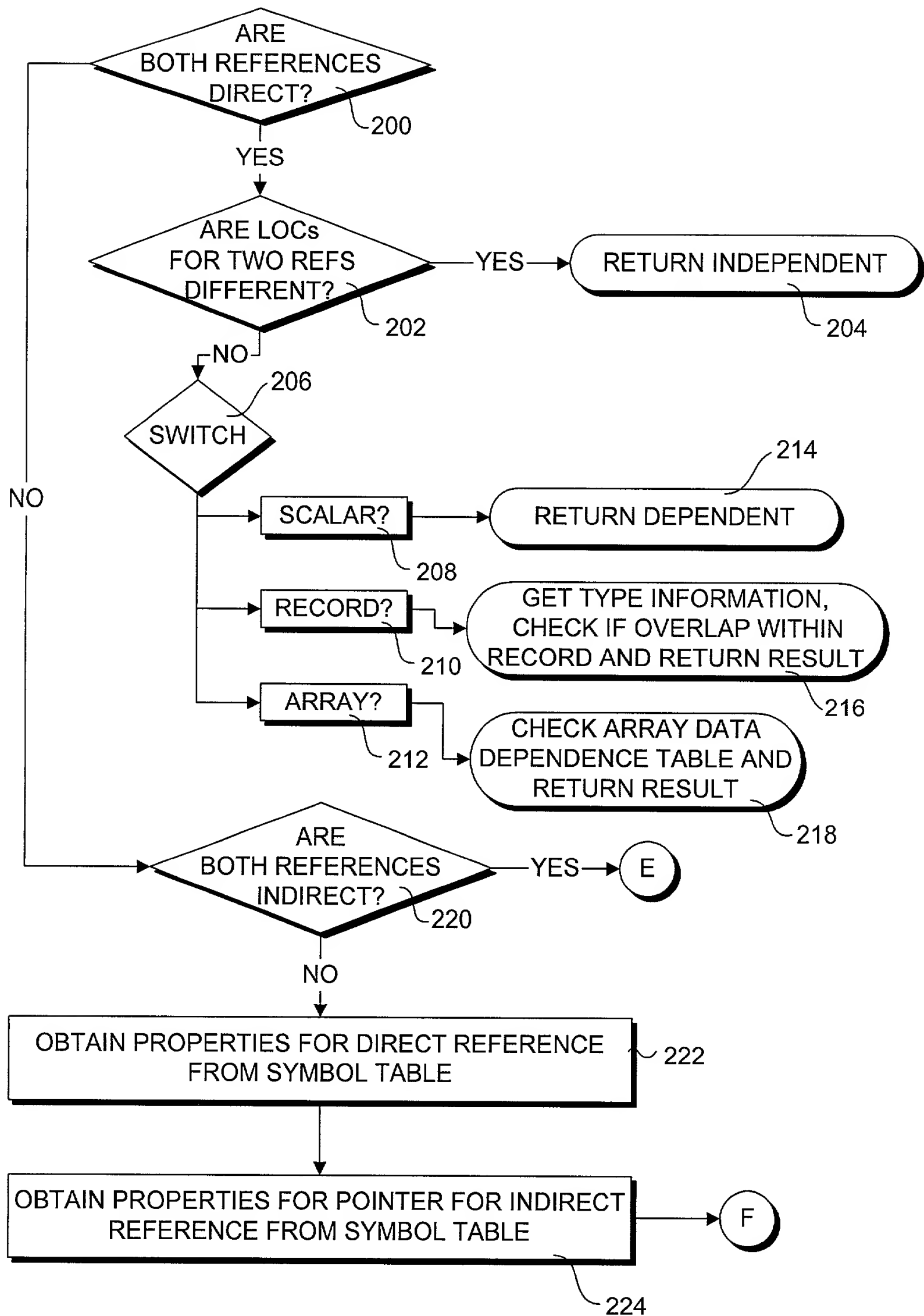
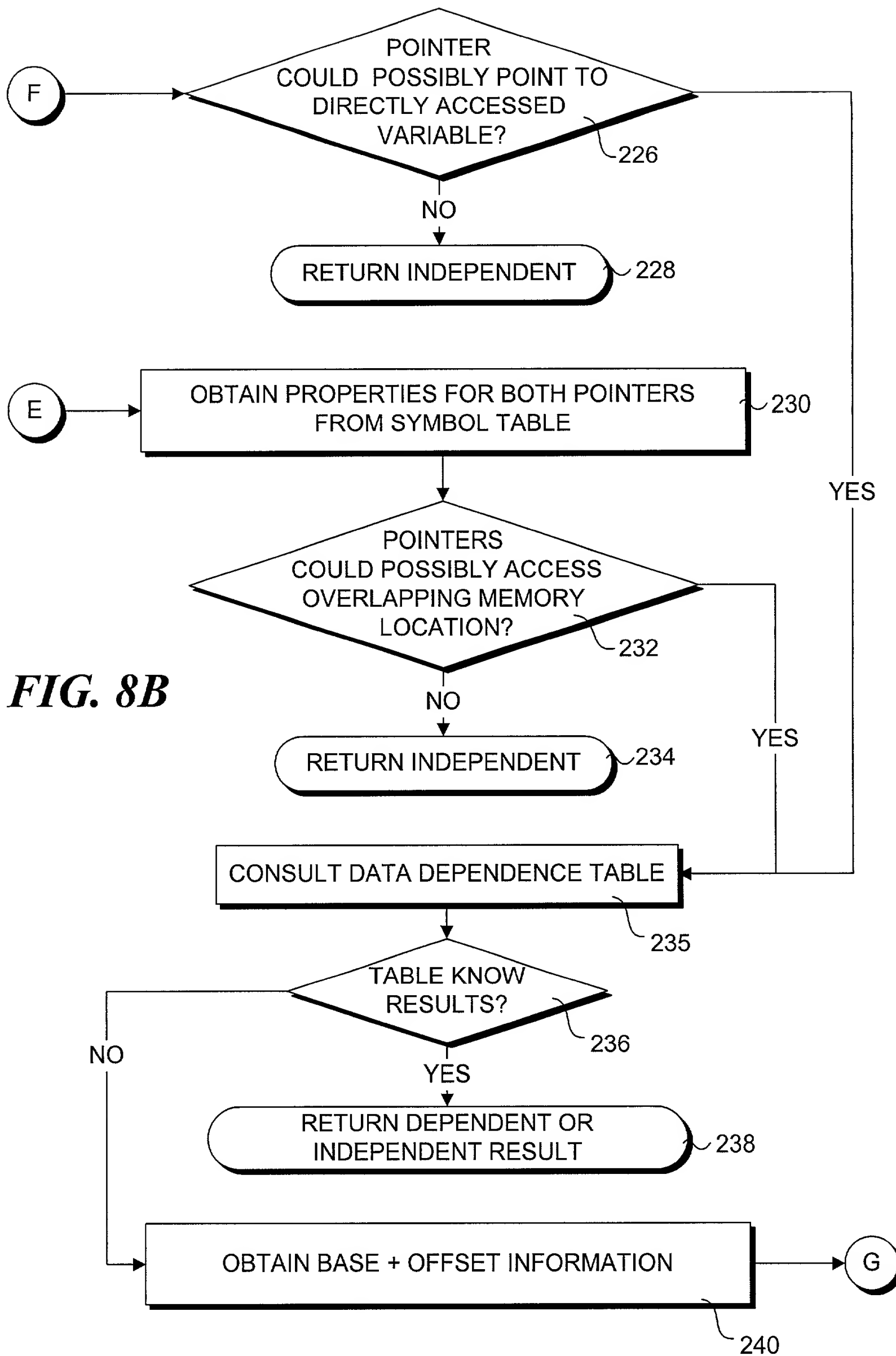


FIG. 8A



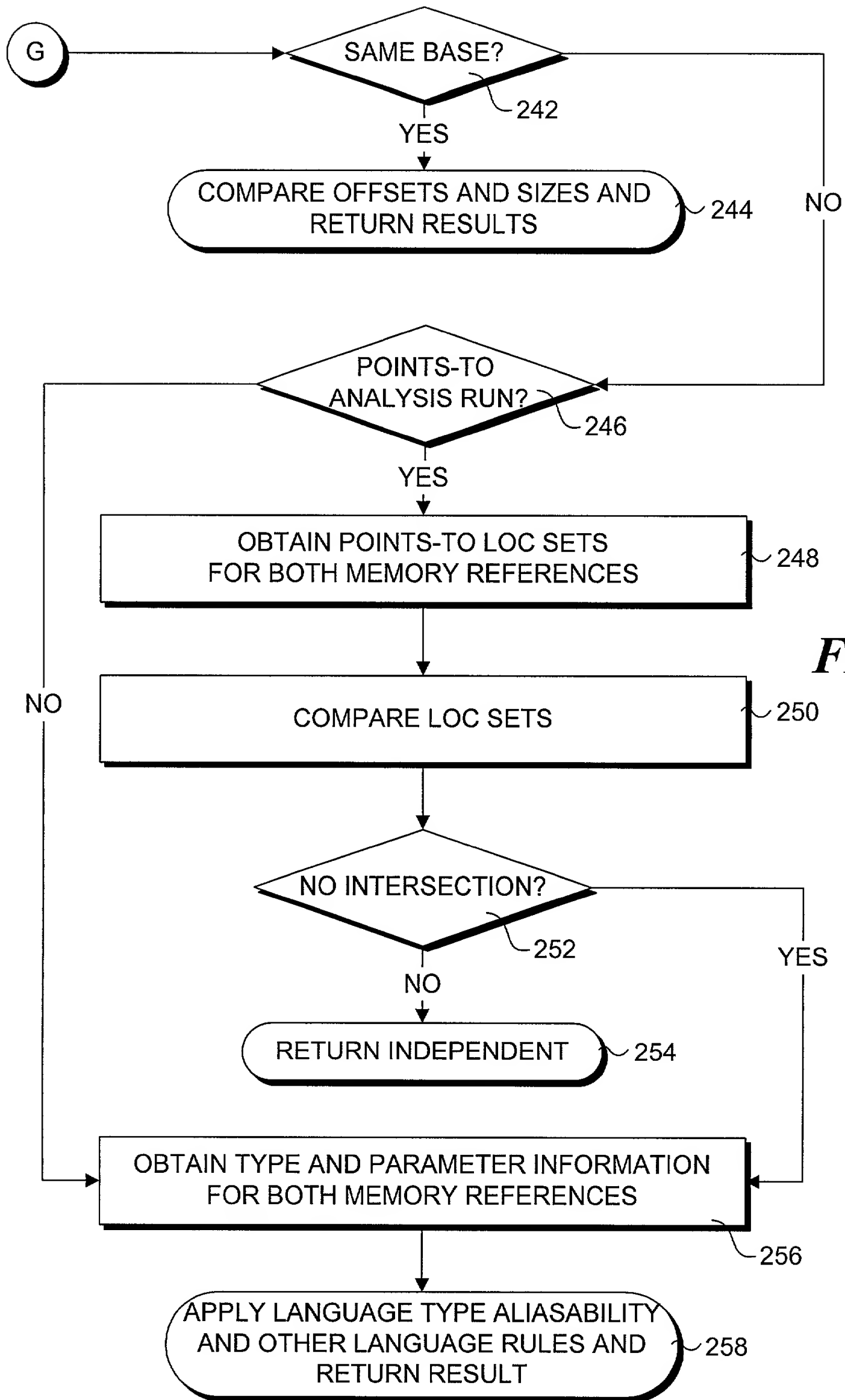


FIG. 8C

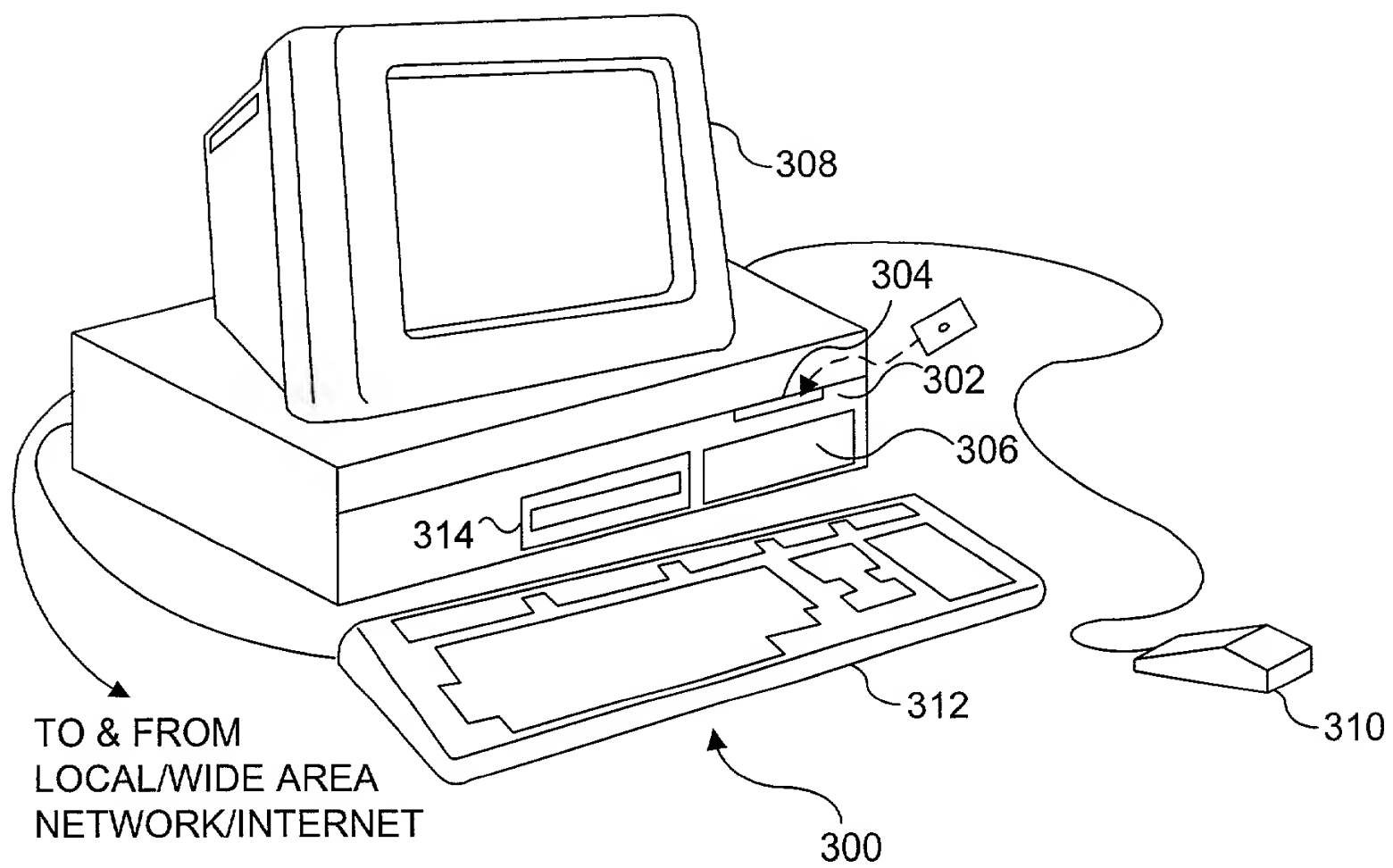


FIG. 9